

SODIMM

A

A

B

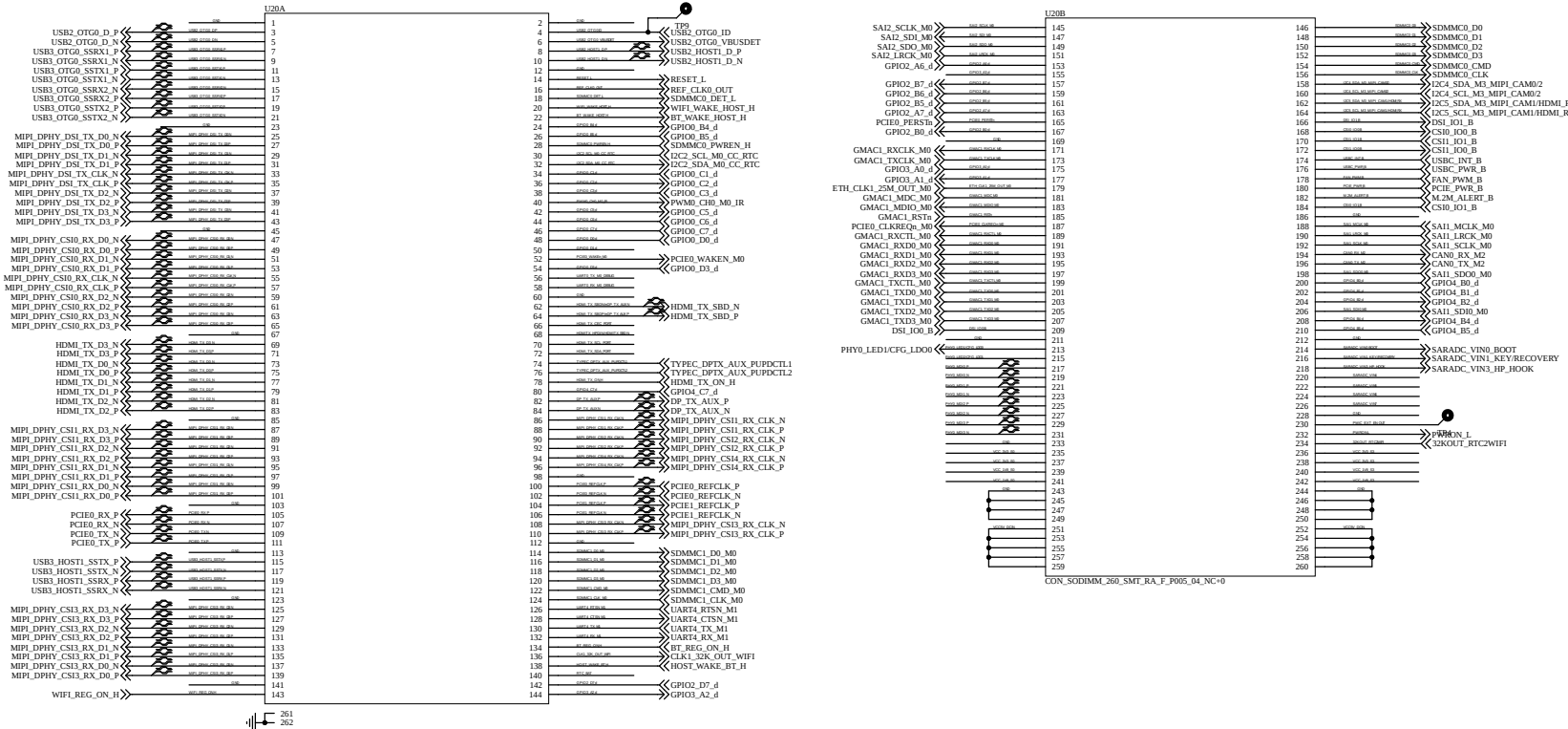
B

C

C

D

D

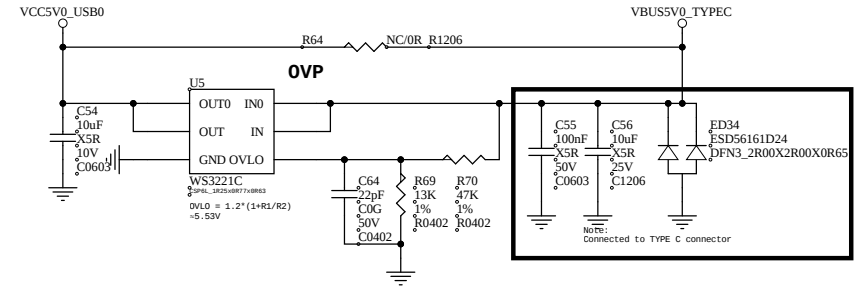
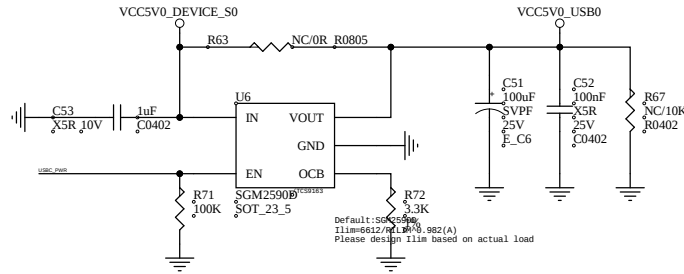


A

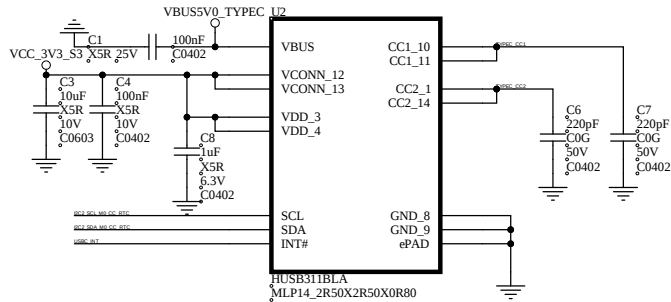
C

USB-C

USB POWER



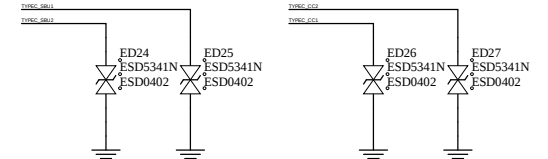
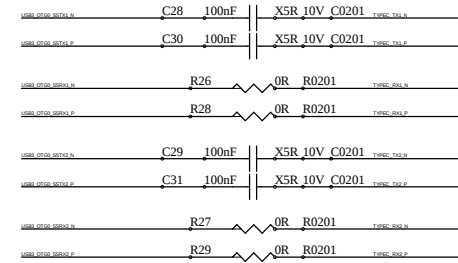
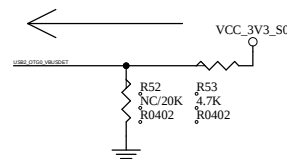
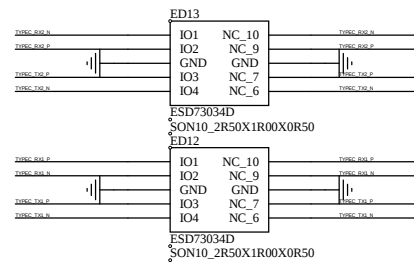
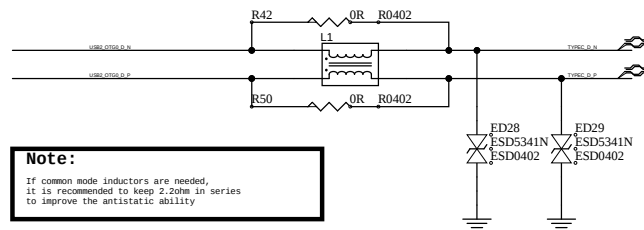
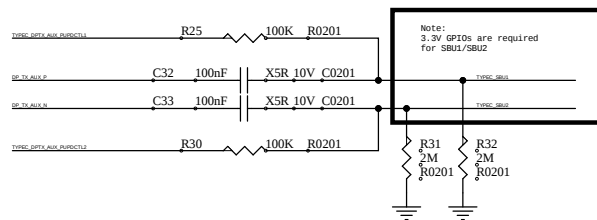
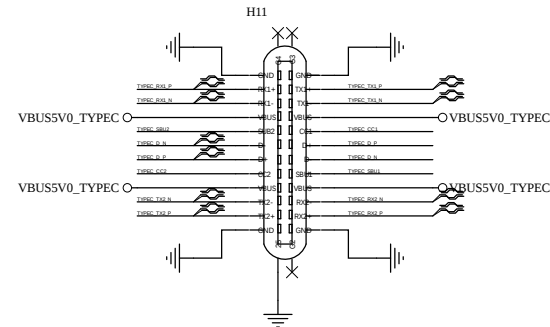
CC



Note:
7bit address=0x4E
VBUS of HUSB311: 4 to 21 TYP = 5V
VDD of HUSB311: 3 to 5.5 TYP = 3.3V
VCONN of HUSB311: 3 to 5.5

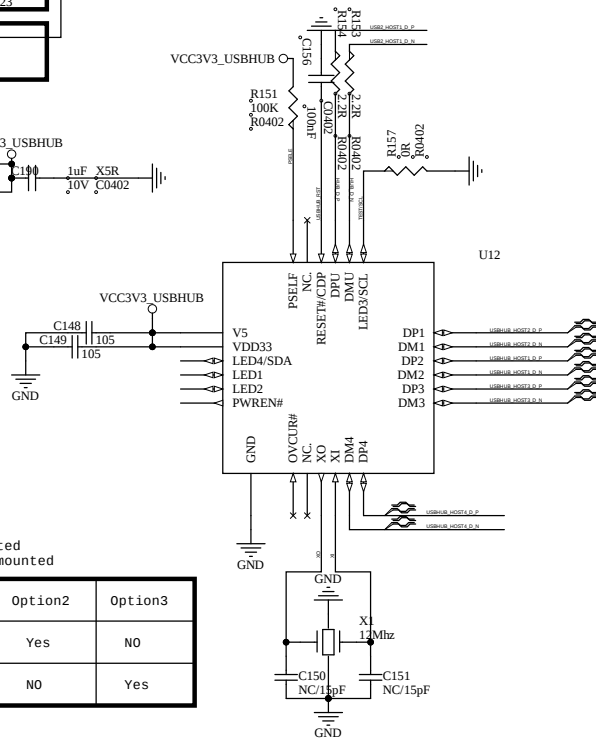
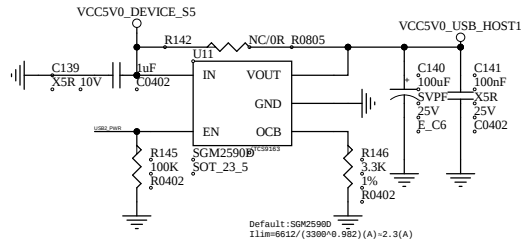
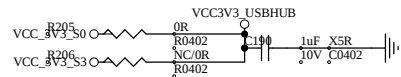
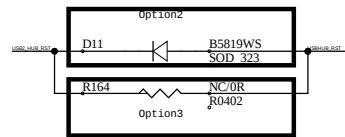
TYPEC

With Displayport Alt Mode
This is also the firmware download interface



USB2 -3

USB Extension



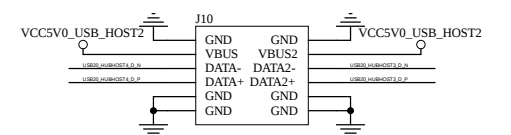
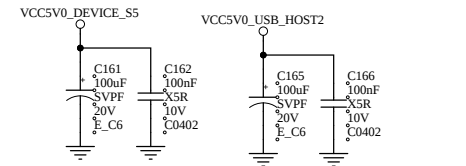
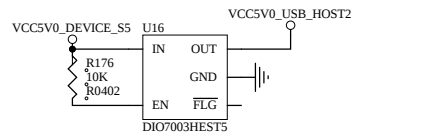
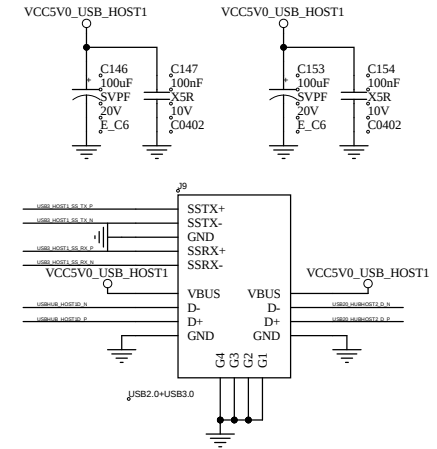
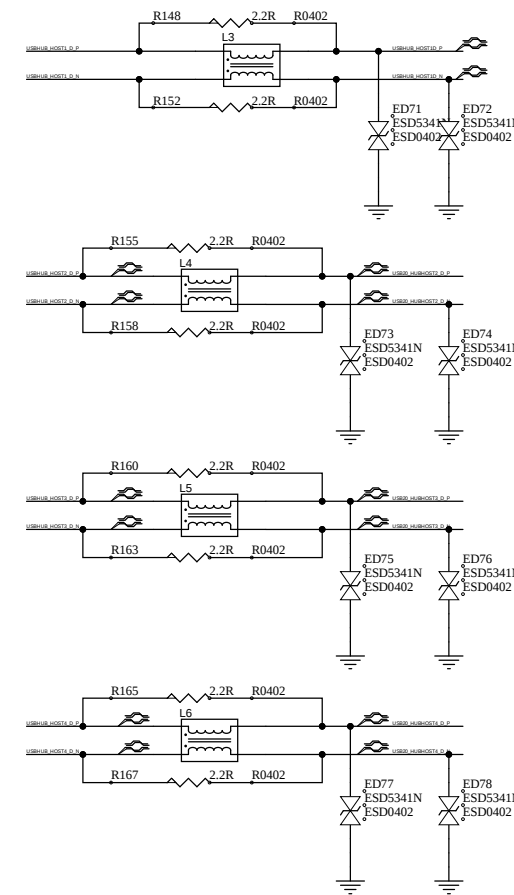
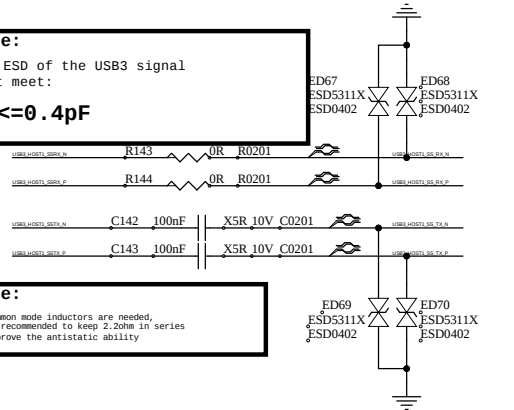
Note:
Yes: option circuit be mounted
No: option circuit not be mounted

OPTION	Option1	Option2	Option3
CH334H	NO	Yes	NO
GL852G	Yes	NO	Yes

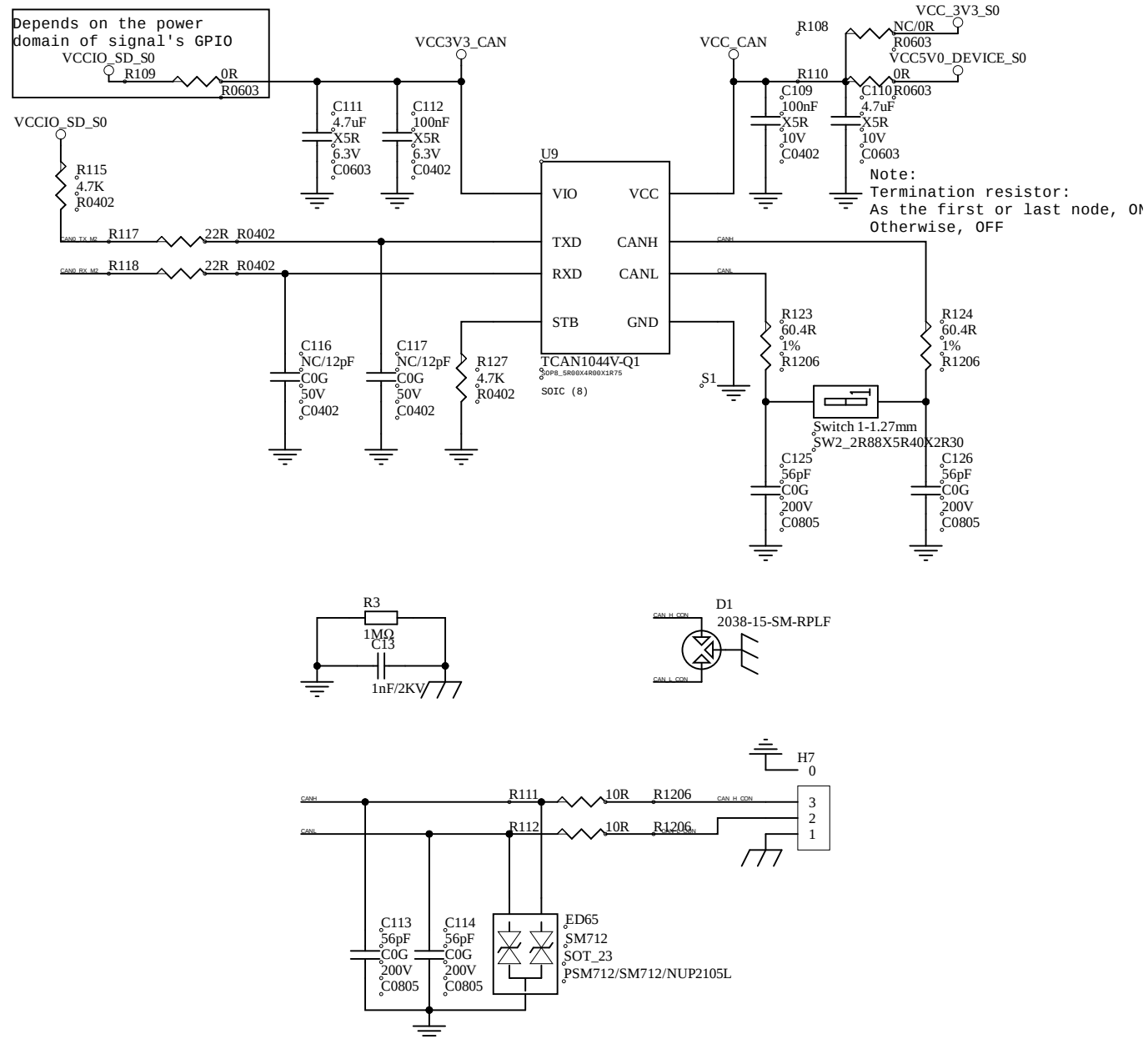
Note:
The ESD of the USB3 signal must meet:

$$C_j \leq 0.4 \text{ pF}$$

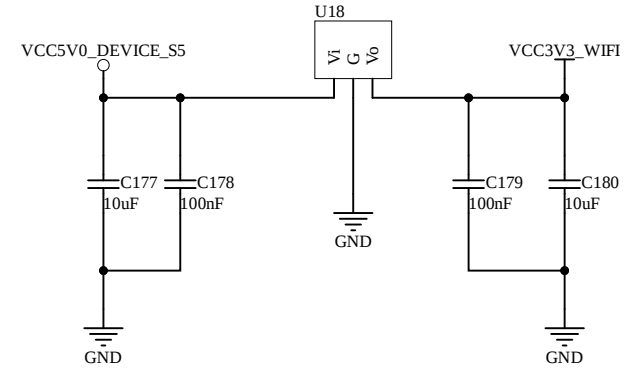
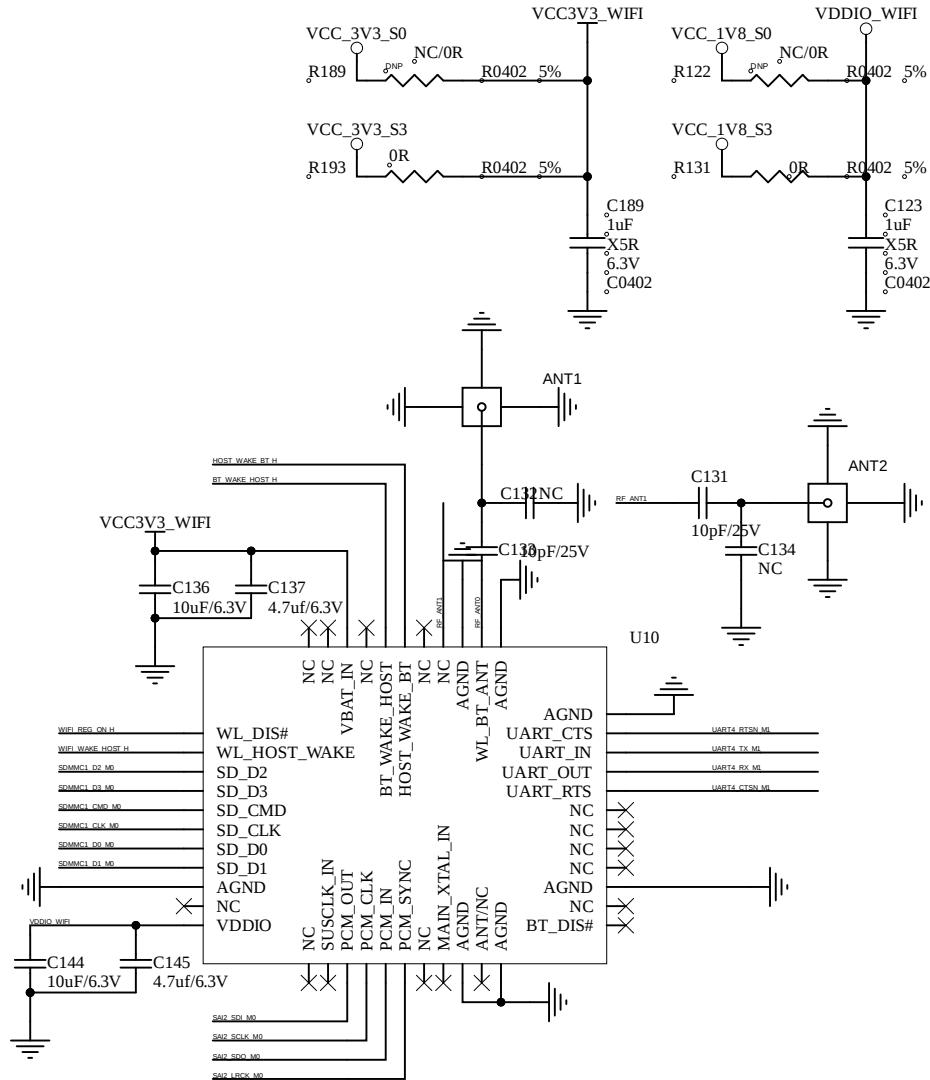
Note:
If common mode inductors are needed, it is recommended to keep 2.2ohm in series to improve the antistatic ability



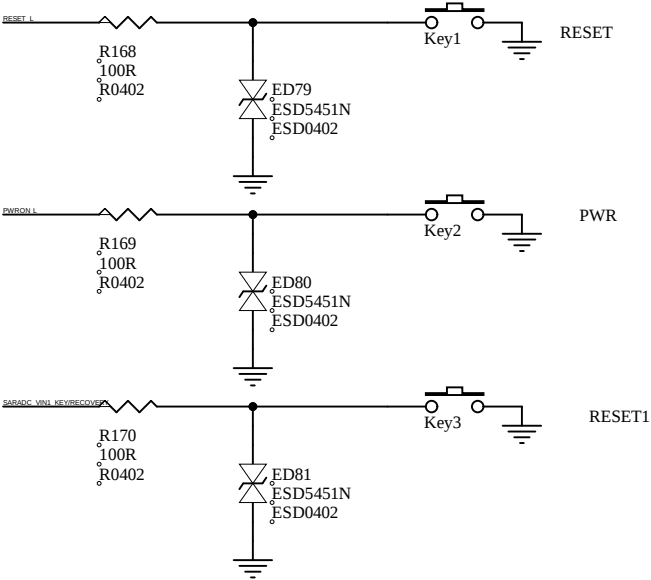
CAN



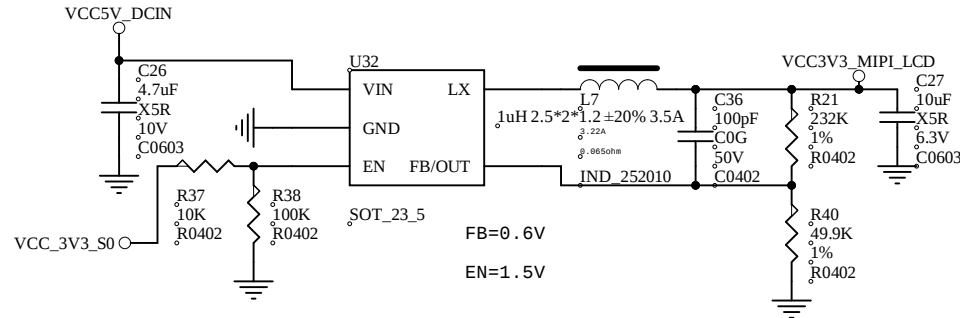
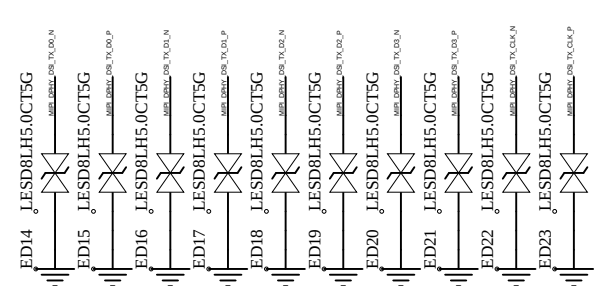
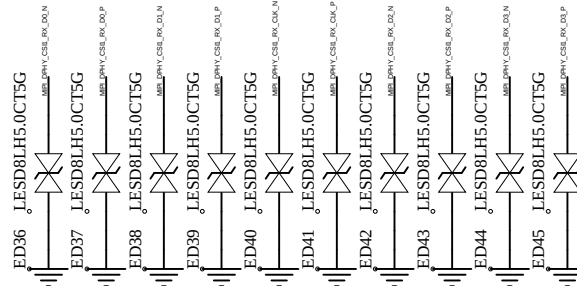
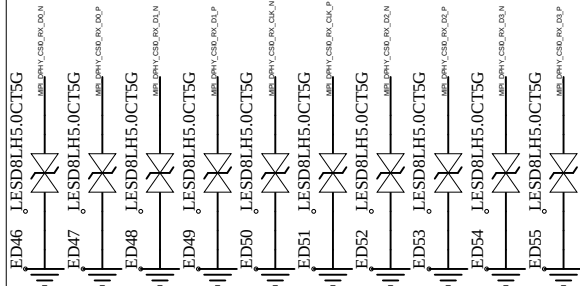
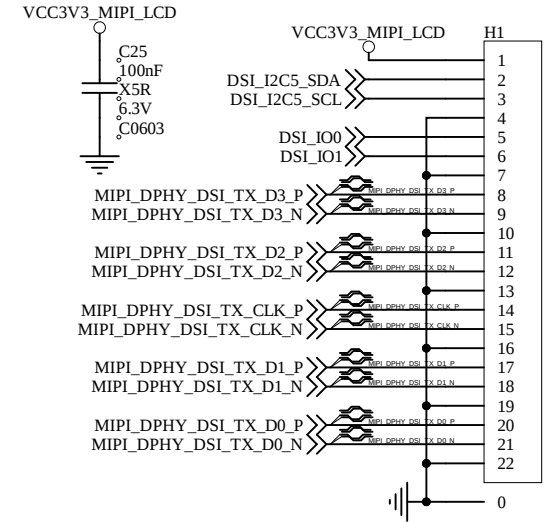
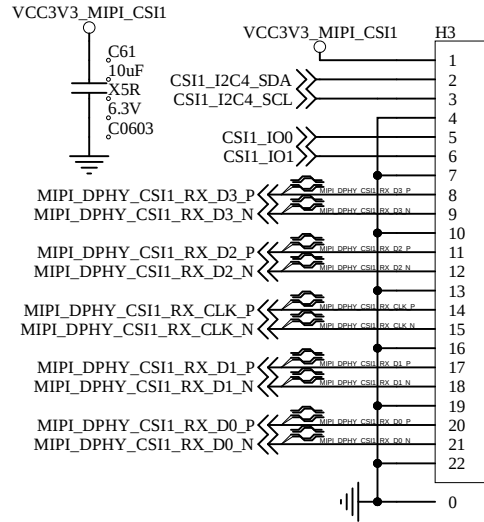
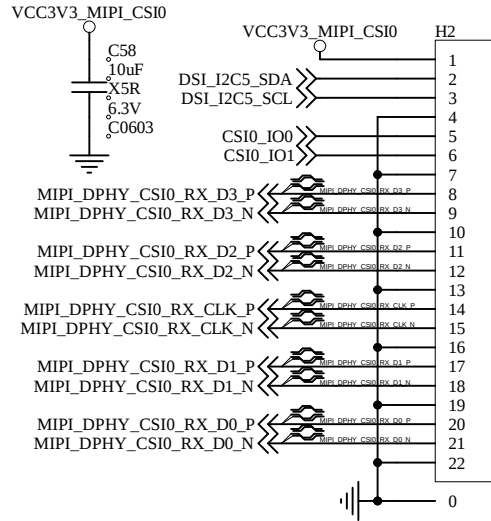
WIFI



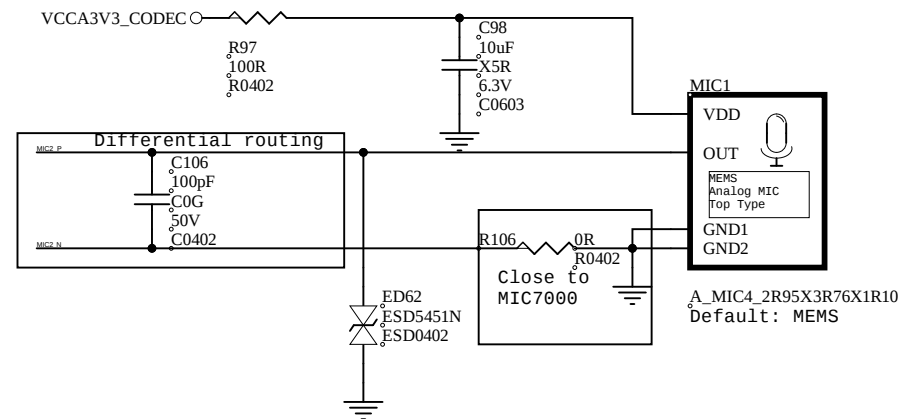
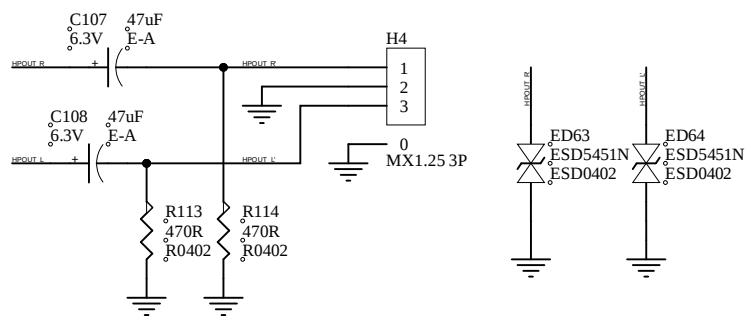
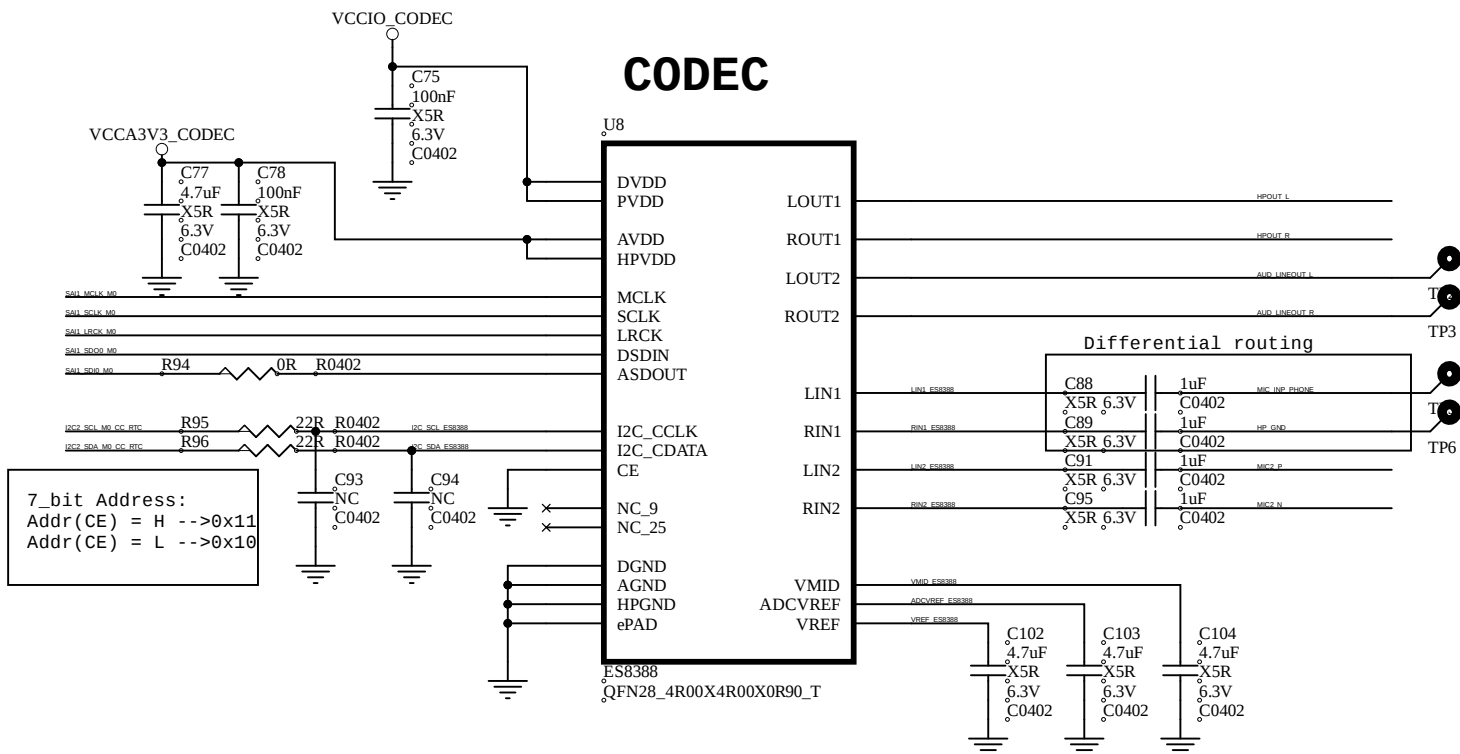
KEY



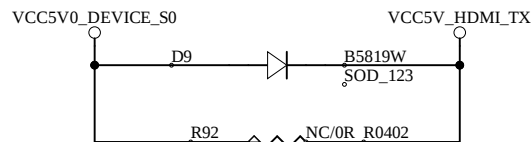
CSI&DSI



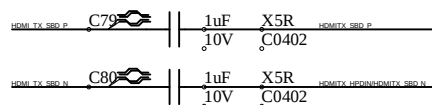
CODEC



HDMI

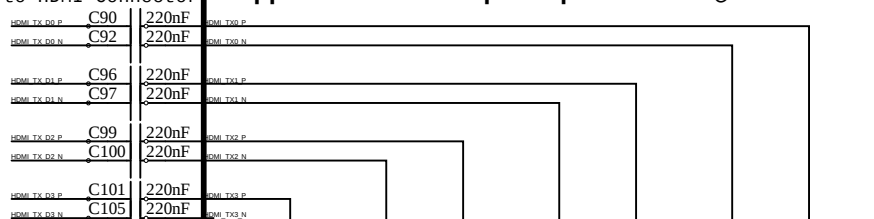


HDMI TX ARC

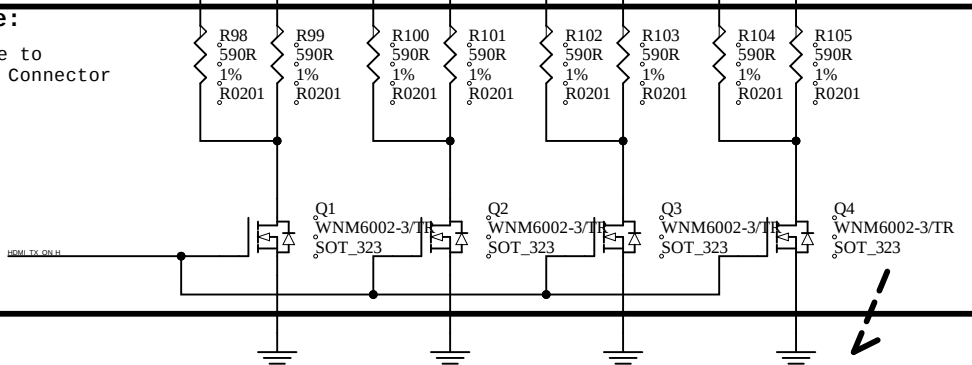


Note:
Close to HDMI Connector

Support video output up to 4Kx2K@120Hz



Note:
Close to HDMI Connector



$R_{ds}=1.7\Omega/2.6V$
 $C_{oss}=7.33pF$

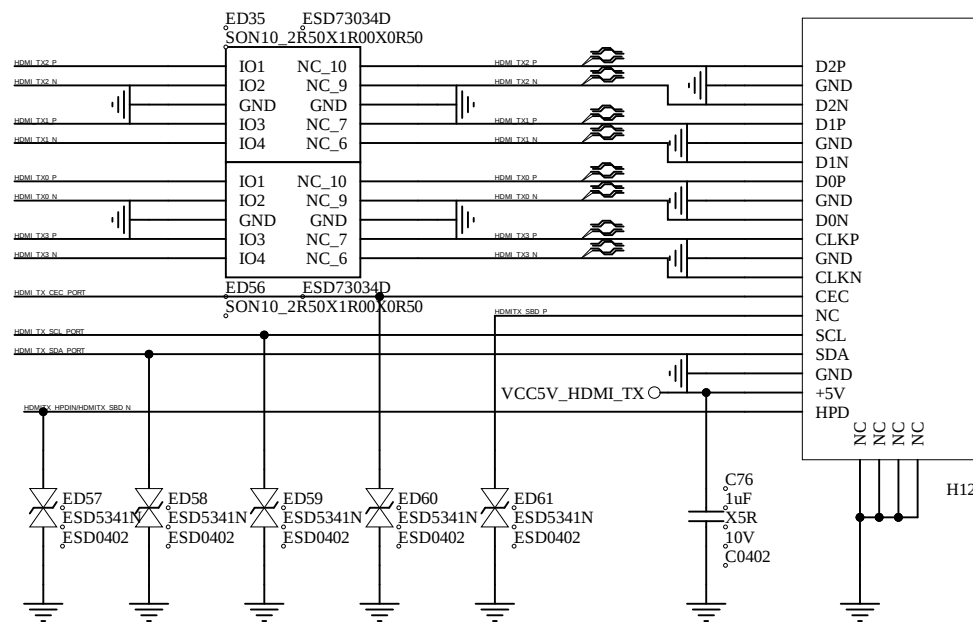
Note:

The controller only support AC coupled link.
In order to backward compatibility or to meet HDMI2.0 (1.4b)
DC common mode spec and Voff, need do R based level-shift.

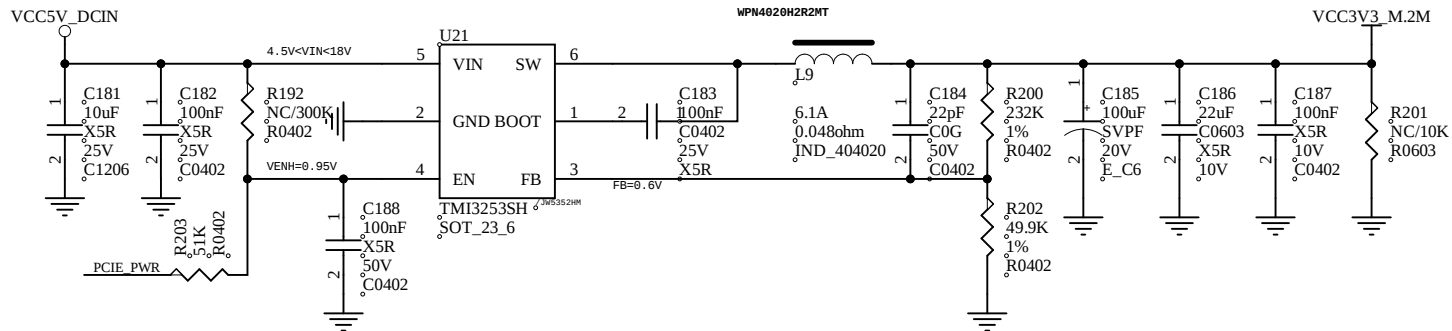
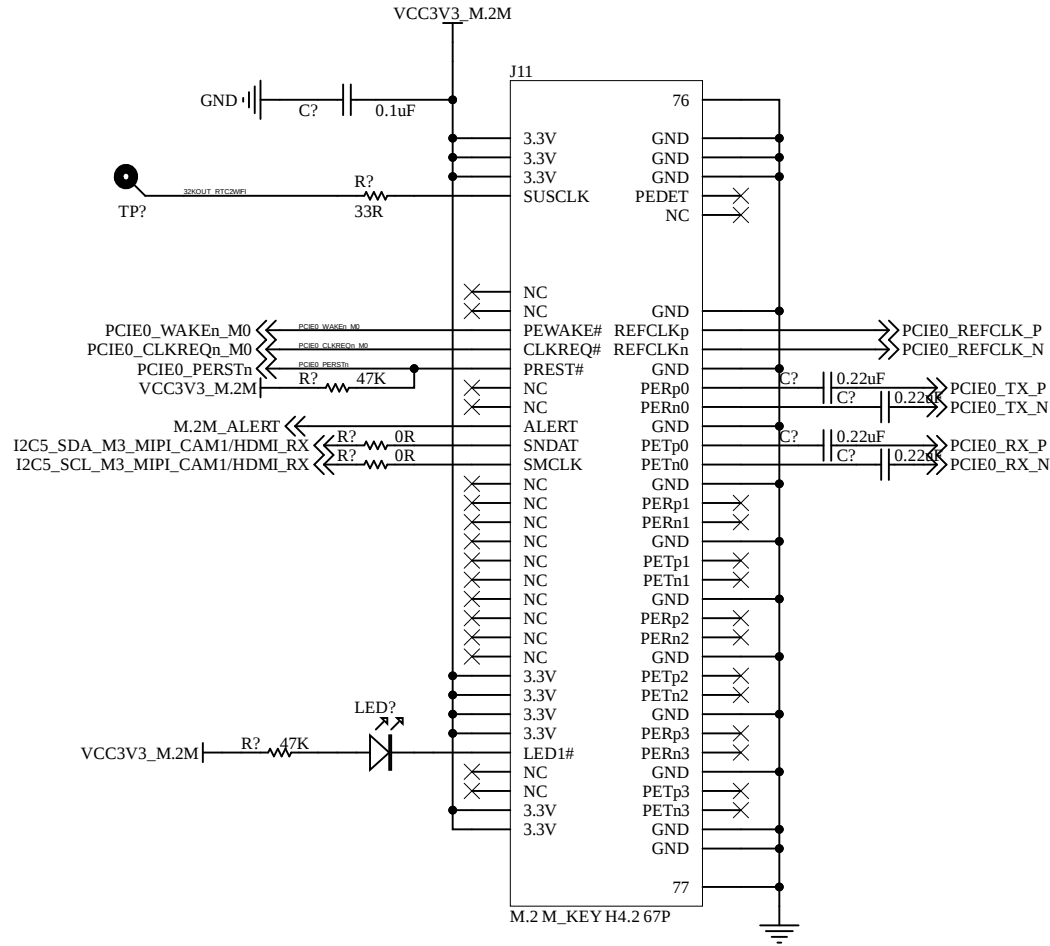
Switch on in HDMI2.0(TMDS) mode

Note:

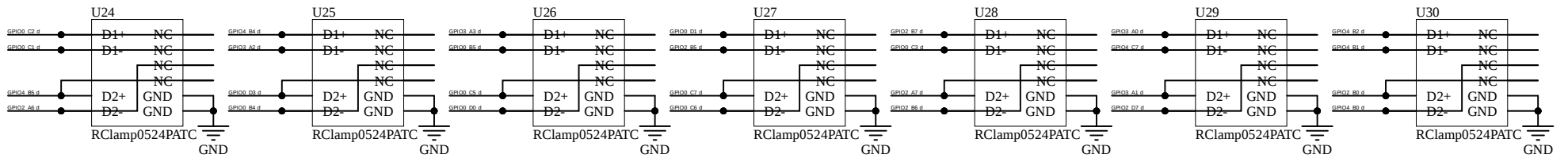
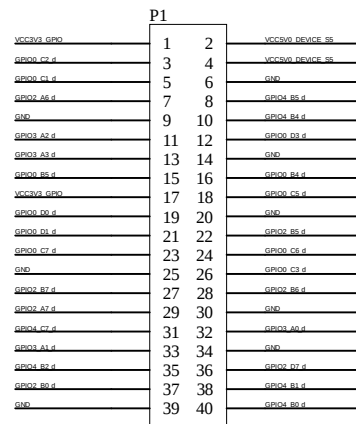
The HDMI2.1 trace length is less than 100mm.
The HDMI2.1 differential trace impedance is 100



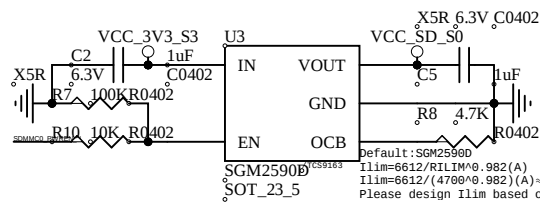
M.2 M Key



40PIN

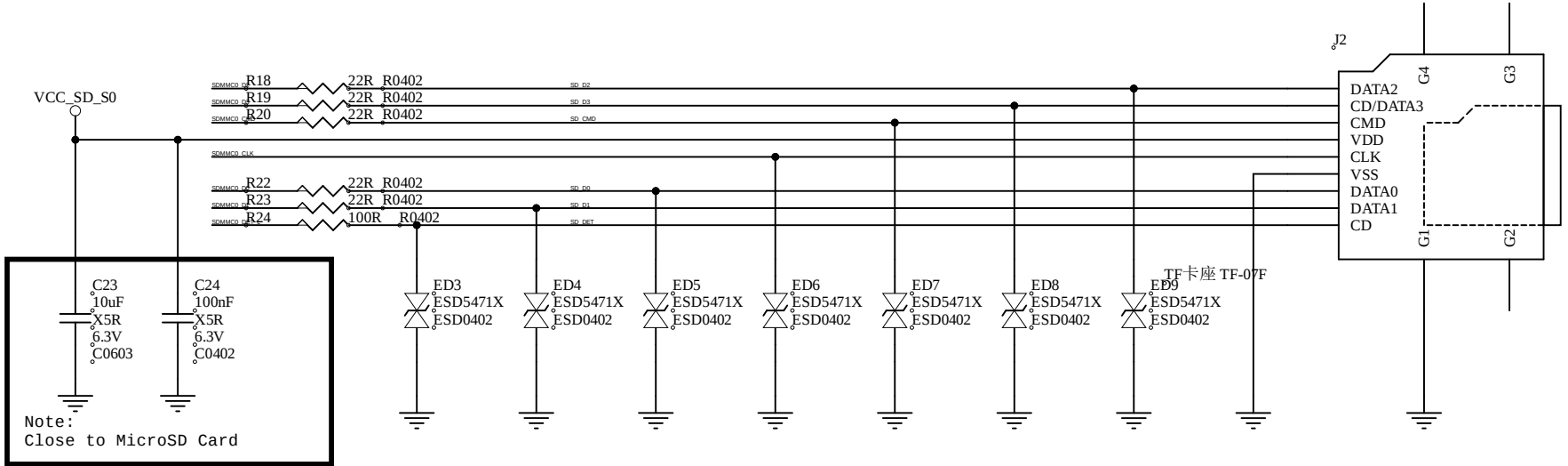


TF - CARD

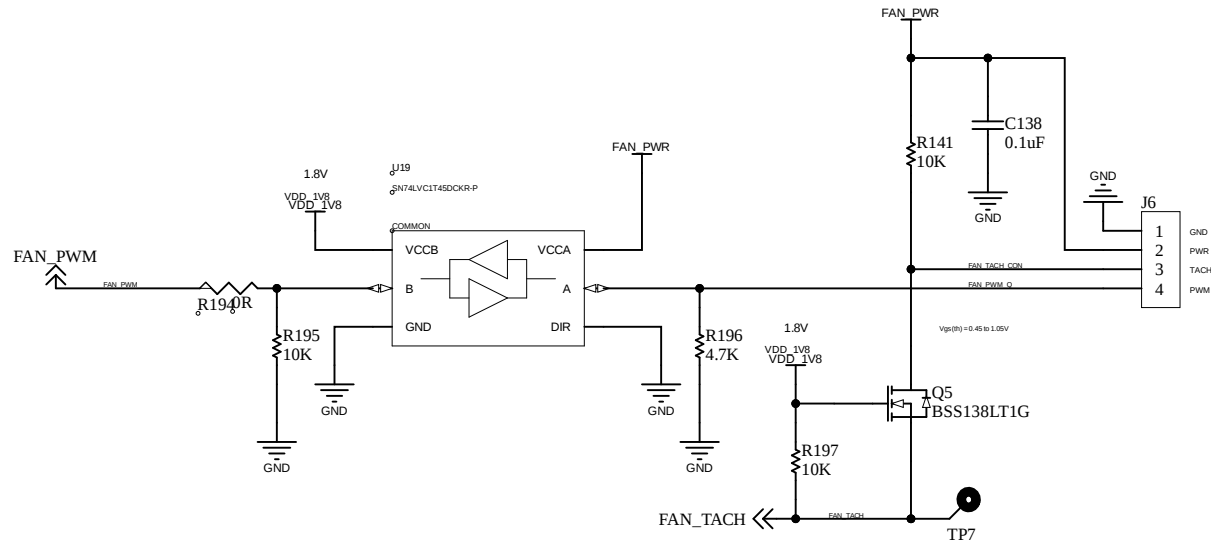


Note:
SDMMC_PWREN=H VCC3V3_SD(Default)
SDMMC_PWREN=L VCC3V3_SD=0V

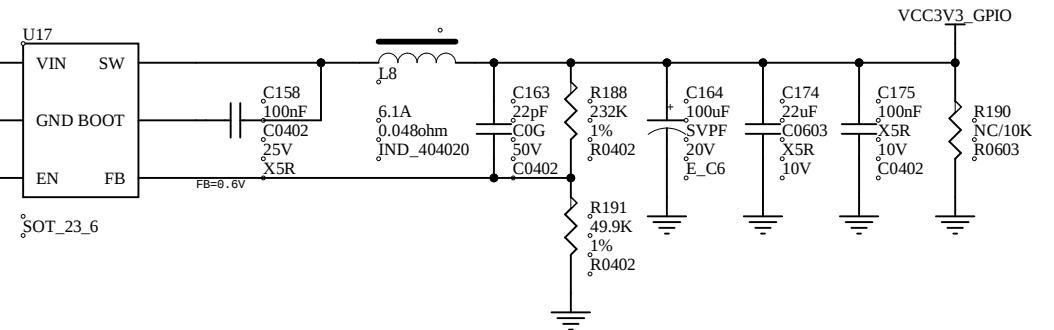
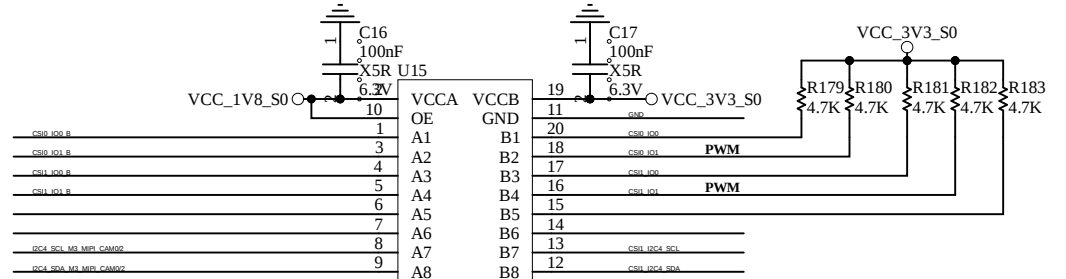
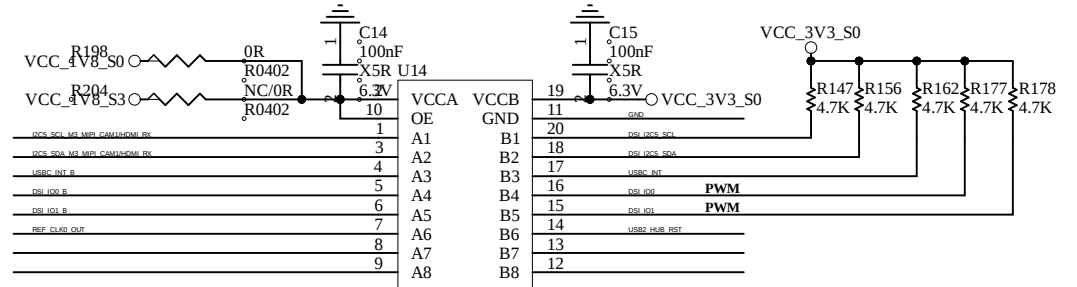
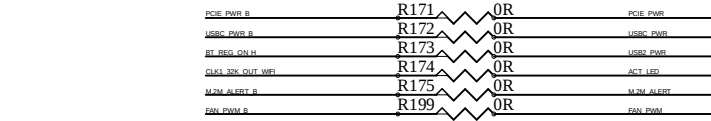
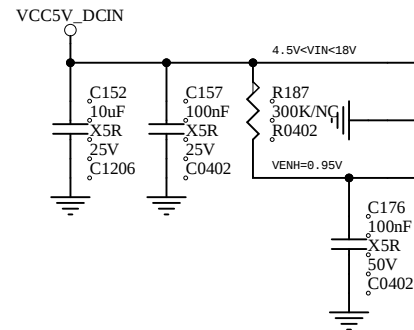
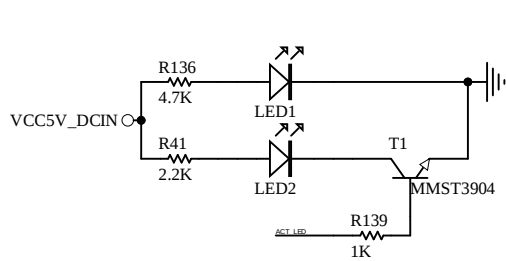
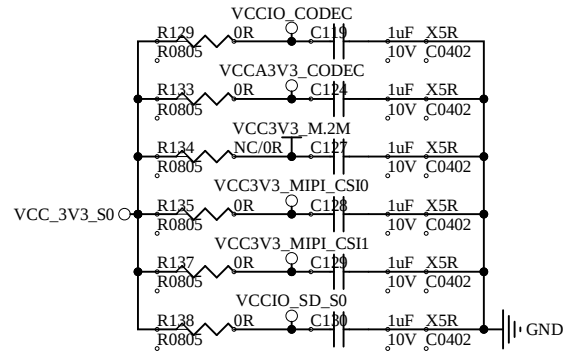
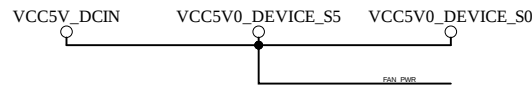
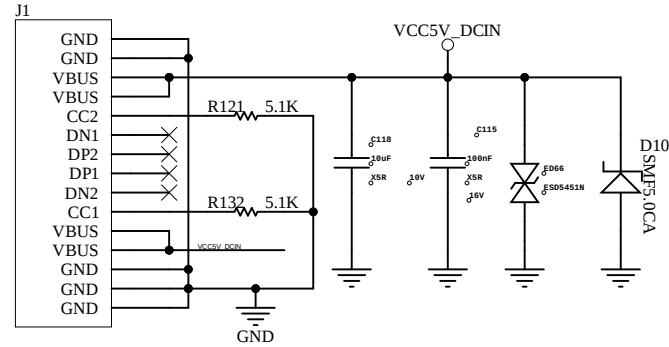
Note:
SDMMC_DET_L:
SDCARD PLUG: Pull-down to GND
SDCARD UNPLUG: Pull-up to PMUI00_VCC1V8.



FAN

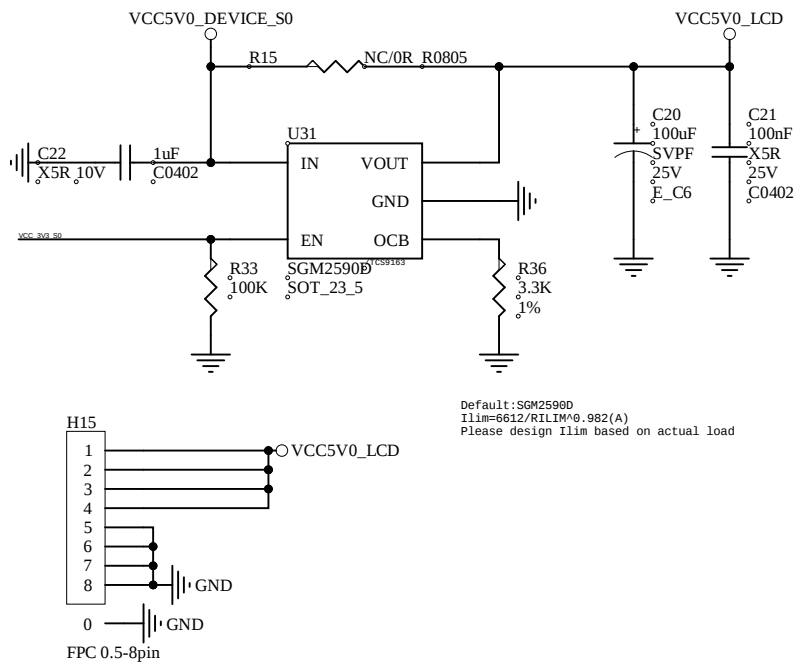


POWER

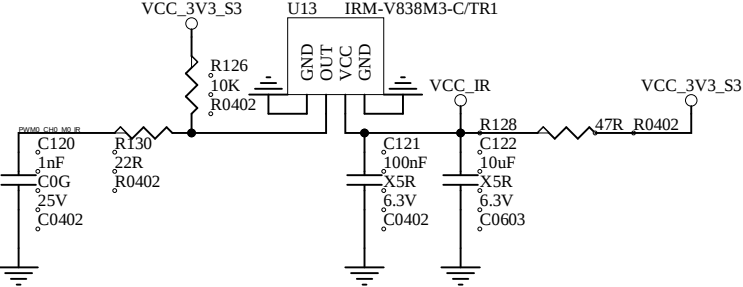


LCD-POWER

LCD POWER

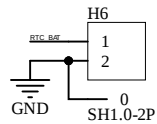


IR

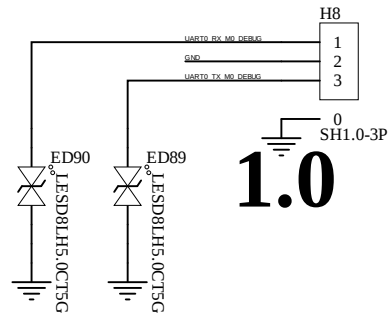


OTHER - INTERFACE

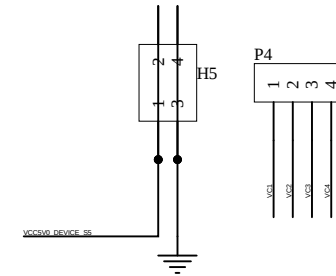
RTC - BAT



DEBUG



POE



ADC

